

	L #	Hits	Search Text	DBs
1	L1	304087	"257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
2	L8	0	5 and offset near (spacer? or sidewall?)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
3	L7	2	5 and thin near (spacer? or sidewall?)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
4	L9	124	(doris-bruce-b or kanarsky-thomas-s or zhang-ying or zhu-huilong or leong-meikei or dokumaci-omer).in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
5	L11	0	((thin near channel) near (SOI or silicon-on-insulator) near (MOSFET? or CMOS or device? or transistor)).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
6	L12	410	((SOI or silicon-on-insulator) near (MOSFET? or CMOS or device? or transistor)).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
7	L14	1	13 and raised near (source or drain or source/drain)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
8	L2	2405	1 and (SOI or silicon-on-insulator) near (MOSFET? or CMOS or device? or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
9	L5	81	2 and raised near (source or drain or source/drain)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
10	L10	2	9 and (thin or offset) near (spacer? or sidewall?)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
11	L13	6	12 and (thin near channel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
12	L15	3673	(SOI or silicon-on-insulator) near (MOSFET? or CMOS or device? or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
13	L16	102	15 and (thin near channel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

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14	L18	0	17 and (thin or offset) near (spacer? or sidewall?)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
15	L6	1	5 and double near spacer?	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
16	L19	1	17 and double near spacer?	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
17	L17	18	16 and raised near (source or drain or source/drain)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB